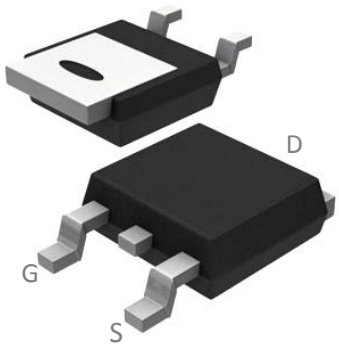
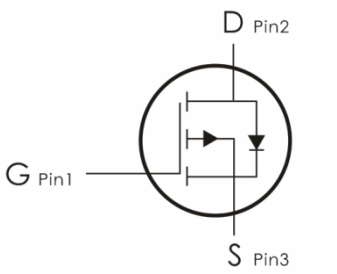
## Description：

This P-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.




## Features：

1) $V_{DS}$=-30V,$I_D$=-70A,$R_{DS(ON)}$<10 mΩ @$V_{GS}$=-10V
2) Low gate charge.
3) Green device available.
4) Advanced high cell denity trench technology for ultra $R_{DS(ON)}$.
5) Excellent package for good heat dissipation.

## Absolute Maximum Ratings： ($T_C$=25℃ unless otherwise noted)

| Symbol | Parameter | Ratings | Units |
|---|---|---|---|
| $V_{DS}$ | Drain-Source Voltage | -30 | V |
| $V_{GS}$ | Gate-Source Voltage | ±20 | V |
| $I_D$ | Continuous Drain Current | -70 | A |
| | Continuous Drain Current-TC=100℃ | -44 | |
| | Pulsed Drain Current[1] | -280 | |
| $E_{AS}$ | Single Pulse Avalanche Energy | --- | mJ |
| $P_D$ | Power Dissipation | 73.5 | W |
| $T_J$, $T_{STG}$ | Operating and Storage Junction Temperature Range | -55 to +150 | ℃ |

## Thermal Characteristics：

| Symbol | Parameter | Max | Units |
|---|---|---|---|
| $R_{\theta JC}$ | Thermal Resistance,Junction to Case | 1.7 | ℃/W |
| $R_{\theta JA}$ | Thermal Resistance,Junction to Ambient | 62 | |

## Package Marking and Ordering Information：

| Part NO. | Marking | Package |
|---|---|---|
| DOD403 | D403 | TO-252 |

## Electrical Characteristics： ($T_C$=25℃ unless otherwise noted)

| Symbol | Parameter | Conditions | Min | Typ | Max | Units |
|---|---|---|---|---|---|---|
| **Off Characteristics** | | | | | | |
| $BV_{DSS}$ | Drain-Sourtce Breakdown Voltage | $V_{GS}$=0V,$I_D$=250μA | -30 | --- | --- | V |
| $I_{DSS}$ | Zero Gate Voltage Drain Current | $V_{GS}$=0V, $V_{DS}$=-30V | --- | --- | -1 | μA |
| $I_{GSS}$ | Gate-Source Leakage Current | $V_{GS}$=±20V, $V_{DS}$=0A | --- | --- | ±100 | nA |
| **On Characteristics** | | | | | | |
| $V_{GS(th)}$ | GATE-Source Threshold Voltage | $V_{GS}$=$V_{DS}$, $I_D$=250μA | -1.2 | -1.6 | -2.5 | V |
| $R_{DS(ON)}$ | Drain-Source On Resistance | $V_{GS}$=-10V,$I_D$=-10A | --- | 7.1 | 10 | mΩ |
| | | $V_{GS}$=-4.5V,$I_D$=-8A | --- | 11.5 | 14 | |
| $G_{FS}$ | Forward Transconductance | $V_{DS}$ =-10V, $I_D$=-10A | --- | 14 | --- | S |
| **Dynamic Characteristics** | | | | | | |
| $C_{iss}$ | Input Capacitance | $V_{DS}$=-15V, $V_{GS}$=0V, f=1MHz | --- | 3300 | 4800 | pF |
| $C_{oss}$ | Output Capacitance | | --- | 410 | 700 | |
| $C_{rss}$ | Reverse Transfer Capacitance | | --- | 280 | 500 | |
| **Switching Characteristics** | | | | | | |
| $t_{d(on)}$ | Turn-On Delay Time | $V_{DD}$=-15V, $I_D$=-1A, $R_{GEN}$=6Ω, $V_{GS}$=-10V | --- | 24.5 | 38 | ns |
| $t_r$ | Rise Time | | --- | 10.5 | 16 | ns |
| $t_{d(off)}$ | Turn-Off Delay Time | | --- | 156.8 | 230 | ns |
| $t_f$ | Fall Time | | --- | 50 | 75 | ns |
| $Q_g$ | Total Gate Charge | $V_{GS}$=-4.5V, $V_{DS}$=-15V, $I_D$=-10A | --- | 35 | 56 | nC |
| $Q_{gs}$ | Gate-Source Charge | | --- | 10.8 | 16 | nC |
| $Q_{gd}$ | Gate-Drain “Miller” Charge | | --- | 10.6 | 16 | nC |
| **Drain-Source Diode Characteristics** | | | | | | |
| $V_{SD}$ | Source-Drain Diode Forward Voltage[2] | $V_{GS}$=0V,$I_S$=-1A, $T_J$=25℃ | --- | --- | -1 | V |

| | | | | | | |
|---|---|---|---|---|---|---|
| Is | Diode Forward Current(Note 2) | $V_G$=$V_D$=0V , Force Current | --- | --- | -70 | A |
| Lsm | Pulsed Source Current | $V_{GS}$=0V , $I_S$=-1A , $T_J$=25℃ | --- | --- | -140 | Nc |

## Notes:

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%.
3. Essentially independent of operating temperature.

## Typical Characteristics: ($T_C$=25℃ unless otherwise noted)

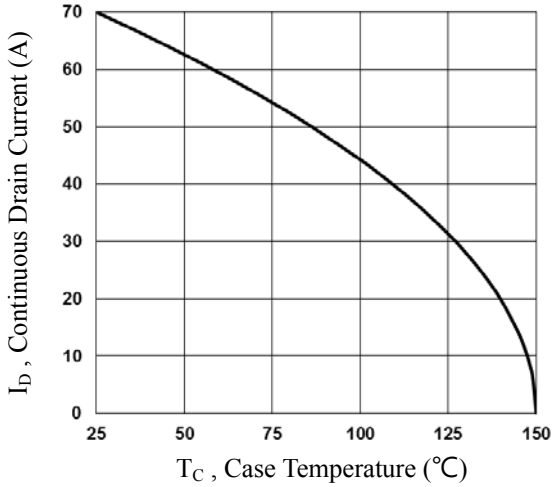



Fig.1 Continuous Drain Current vs. $T_C$

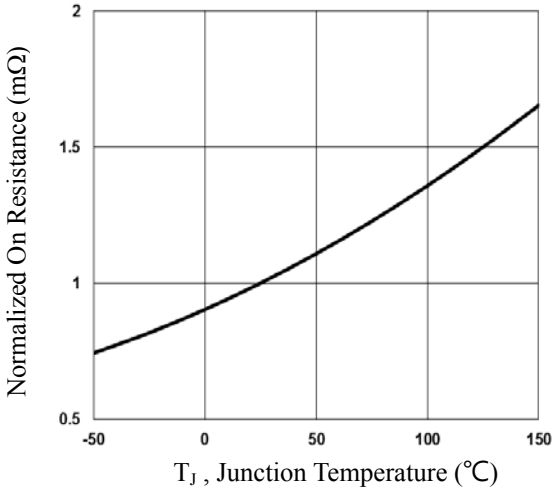



Fig.2 Normalized RDSON vs. $T_J$

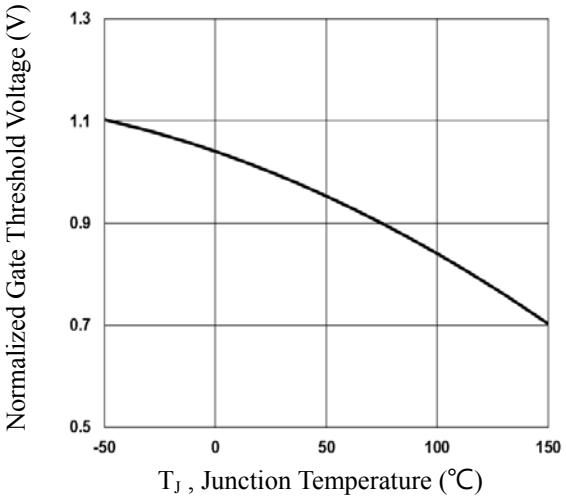



Fig.3 Normalized $V_{th}$ vs. $T_J$

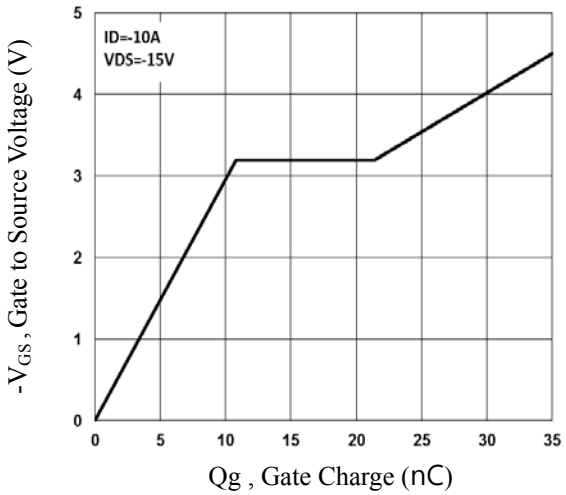



Fig.4 Gate Charge Waveform

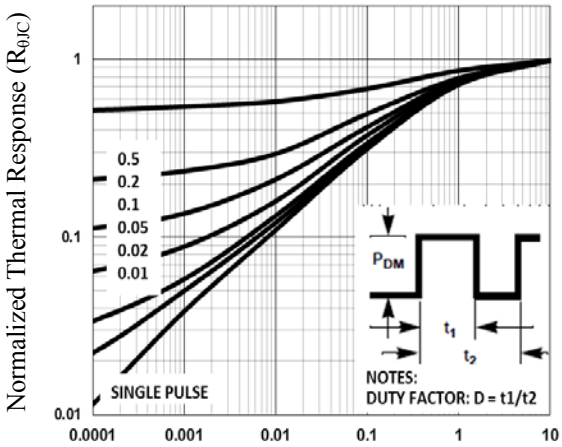



Fig.5 Normalized Transient Impedance

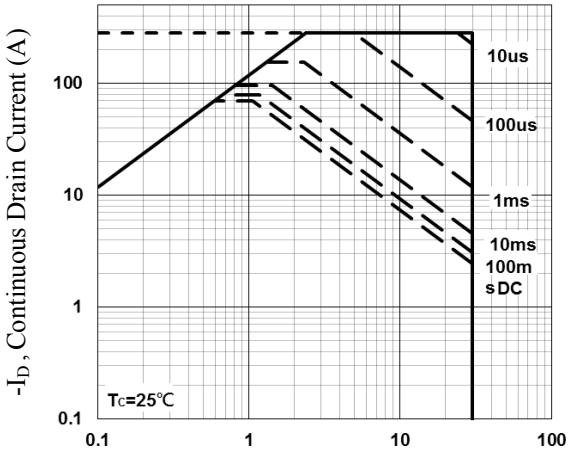



Fig.6 Maximum Safe Operation Area

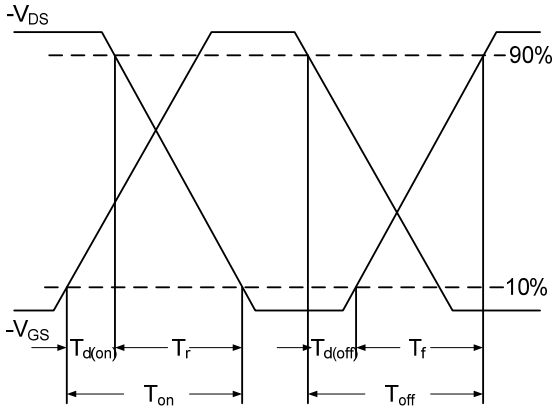



Fig.7 Switching Time Waveform

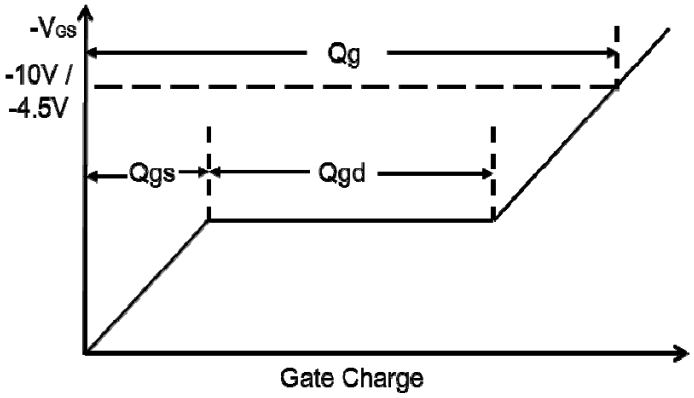



Fig.8 Gate Charge Waveform